





PAD	CONNECTION
1	Control voltage
2	Enable / disable
3	Ground
4	Output 1 (Q)
5	Output 2 (\bar{Q})
6	Supply

Scale 2:1

Features

-  **Frequencies up to 700MHz**
-  **Complementary LV-PECL outputs**
-  **Fast rise / fall times**
-  **Enable / disable tristate function**

Enable / Disable Function

Input (pad 2) *	Output 1 (pad 4)	Output 2 (pad 5)
Open	Active	Active
'1' level V_{IH}	High ('1')	High ('0')
'0' level V_{IL}	Active	Active

* Note: '0' level = $V_{IL} \leq V_{CC} - 1.60V$, '1' level = $V_{IH} \geq V_{CC} - 1.10V$

Specifications

Parameters	Product	Option Codes
	GVXO-54	
Frequency range: 12.0 ~ 700MHz	■	
Voltage control (V_{CTL}): +1.65V \pm 1.50V, 10% linearity	■	
Frequency pullability (min): \pm 150ppm Other	■ □	specify
Frequency stability*: \pm 50ppm \pm 25ppm Other	■ □ □	B C specify
Operating temperature range: -10 to +70°C -40 to +85°C	■ □	I
Storage temperature range: -40 to +85°C	■	
Supply voltage (V_{DD}): +3.3V (\pm 5%)	■	
Supply current: 65mA max (\leq 100MHz) 120mA max ($>$ 100 ~ 700MHz)	■ ■	
Output: Complementary PECL	■	
Test load: $R_T = 50\Omega$ $V_T = 1.3V$	■ ■	
Logic levels: '0' level = +1.7V max '1' level = +2.2V min	■ ■	
Waveform symmetry: 40:60 max @ 50% $V_{P,P}$	■	
Rise / fall time: 1.5ns max (20% ~ 80% $V_{P,P}$)	■	
Phase jitter: 5ps max (12kHz ~ 20MHz)	■	

■ Standard. □ Optional - Please specify required code(s) when ordering

* Frequency stability is inclusive of operating temperature range, supply voltage change and ageing, with $V_{CTL} = +1.65V$

Ordering Information

Product + option codes + frequency

eg: **GVXO-54/C 155.520MHz** \pm 25ppm stability -10+70°C

GVXO-54/BI 77.760MHz \pm 50ppm stability -40+85°C

Option codes must be included to specify a model completely.

Option code X (eg GVXO-54/X) denotes a custom spec.

◆ Available on T&R - 2k pcs per reel. Refer to our website for details.